IN THE UNITED STATES PATENT AND TRADEMARK OFFICE Application Serial No.10/050,347 Filing Date January 15, 2002 graveshiventor......Gurtej S. Sandhu et al. Assignee Micron Technology, Inc. Examiner Laura M. Schillinger Attorney's Docket No.MI22-1897 Title: Methods of Forming Transistors

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -- See Attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. Pursuant to Federal Register, Vol. 69, No. 182, pg. 56542 (September 21, 2004), no copies of any cited U.S. patents or U.S. published applications are included herewith. Copies of all other cited art are attached. No admission is made regarding whether the listed references are prior art.

Respectfully submitted,

Bv:

Jennifer J. Taylor. Pb.D.

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APPLICANT

SERIAL NO. 10/050,347

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2813

APPLICANT Gurtej S. Sandhu

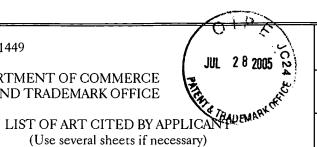
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LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)

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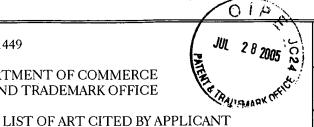
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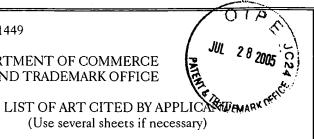
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